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Abstract of the Disclosure:

A chip antenna has pattern antennas A1, A2' which are formed on a plurality of layers of a base member of a stacked structure and of which at least parts of their patterns are not overlapping with each other in the stacked direction, and a feeding terminal 12 which is formed on a surface of the base member and which is connected to the pattern antennas A1, A2'. By deleting an overlap with each other in the stacked direction between their patterns of pattern antennas A1, A2', one pattern antenna can be adjusted to have an optimized resonant frequency without influencing frequency characteristics of another pattern antenna. The pattern antenna A2' has a first area of a rectangular shape and a second area elongating continuously from the first area. Upon adjusting a length of an arm in the direction that the second area elongates in the first area and a length of the second area, a desirable resonant waveform can be obtained.